100 年 4 月 通過 學術審查

93 學年度碩士入學，94 學年度上學期直升 (博六)

Journal Papers

1. **M.H. Wu**, C.Y. Fu, P.C. Wang, and R.S. Tsay, "A Distributed Timing Synchronization Technique for Parallel Multi-Core Instruction-Set Simulation," accepted by *ACM Transactions on Embedded Computing Systems (TECS).* (SCIE/EI)

2. **M.H. Wu**, P.C. Wang, C.Y. Fu, and R.S. Tsay, "An Extended SystemC Framework for Fast and Reliable HW/SW Co-Simulation," under major revision of *ACM Transactions on Design Automation of Electronic Systems (TODAES).* (SCIE/EI)

Conference Papers

1. **M.H. Wu**, P.C. Wang, C.Y. Fu, and R.S. Tsay, "A High-Parallelism Distributed Scheduling Mechanism for Multi-Core Instruction-Set Simulation," accepted by *Proc. of the Design Automation Conference (DAC)*, 2011. **(Acceptance Rate: 22.6%, 156/690)**

2. P.C. Wang, **M.H. Wu**, and R.S. Tsay, "DOM: A Data-Dependency-Oriented Modeling Approach for Efficient Simulation of OS Preemptive Scheduling," *Proc. of the Design Automation and Test in Europe Conference (DATE)*, pp. 335-340, Grenoble, France, Mar. 14-18, 2011. **(Acceptance Rate: 27.0%, 211/781)**

3. C.K. Lo, L.C. Chen, **M.H. Wu**, and R.S. Tsay, "Cycle-Count-Accurate Processor Modeling for Fast and Accurate System-Level Simulation," *Proc. of the Design Automation and Test in Europe Conference (DATE)*, pp. 341-346, Grenoble, France, Mar. 14-18, 2011. **(Acceptance Rate: 27.0%, 211/781)**

4. C.Y. Fu, **M.H. Wu**, and R.S. Tsay, "A Shared-Variable-Based Synchronization Approach to Efficient Cache Coherence Simulation for Multi-Core Systems," *Proc. of the Design Automation and Test in Europe Conference (DATE)*, pp. 347-352, Grenoble, France, Mar. 14-18, 2011. **(Acceptance Rate: 27.0%, 211/781)**

5. **M.H. Wu**, W.C. Lee, C.Y. Chuang, and R.S. Tsay, "Automatic Generation of Software TLM in Multiple Abstraction Layers for Efficient HW/SW Co-simulation," *Proc. of the Design Automation and Test in Europe Conference*

(DATE), pp. 1177-1182, Dresden, Germany, Mar. 8-12, 2010. **(Acceptance Rate: 27.0%, 229/847)**

6. **M.H. Wu**, Y.H. Huang, C.Y. Fu, P.C. Wang, and R.S. Tsay, "A Novel Synchronization Technique for Fast and Accurate Multi-Core Instruction-Set Simulation," *21st VLSI Design/CAD Symposium (VLSI/CAD)*, Kaohsiung, Taiwan, Aug. 3-6, 2010. **(Best Paper Nomination)**

7. **M.H. Wu**, C.Y. Fu, P.C. Wang, and R.S. Tsay, "An Effective Synchronization Approach for Fast and Accurate Multi-core Instruction-set Simulation," *Proc. of the International Conference on Embedded Software (EMSOFT)*, pp. 197-204, Grenoble, France, Oct. 12-16, 2009. **(Acceptance Rate: 31.1%, 33/106)**

100 年 4 月 通過 學術審查

94 學年度碩士入學，95 學年度上學期直升 (博五)

Journal Papers:

1. Chi Ta Wu, **Ang-Chih Hsieh** and TingTing Hwang, "Instruction Buffering for Nested Loops in Low-Power Design," *IEEE Transactions on Very Large Scale Integration Systems (TVLSI)*, Volume 14, Issue 7, pp. 780-784, July 2006.
   - **SCI, EI**

2. **Ang-Chih Hsieh**, Tzu-Teng Lin, Tsuang-Wei Chang and TingTing Hwang, "A Functionality-Directed Clustering Technique for Low-Power MTCMOS Design — Computation of Simultaneously Discharging Current," *ACM Transactions on Design Automation of Electronic Systems (TODAES)*, Volume 12, Issue 3, August 2007.
   - **SCI, EI**

3. **Ang-Chih Hseih** and TingTing Hwang, "TSV Redundancy: Architecture and Design Issues in 3D IC," accepted by *IEEE Transactions on Very Large Scale Integration Systems (TVLSI)*.
   - **SCI, EI**
   - **Chapter 4**

4. **Ang-Chih Hsieh** and TingTing Hwang, "Run-Time Reconfiguration of Expandable Cache for Embedded Systems," submitted to *IEEE Transactions on Very Large Scale Integration Systems (TVLSI)*.
   - **Major revision**, **SCI, EI**
   - **Chapter 2**

5. **Ang-Chih Hsieh** and TingTing Hwang, "Thermal-Aware Memory Mapping in 3D Designs," submitted to *ACM Transactions on Embedded Computing Systems (TECS)*.
   - **Under Review, SCI, EI**
   - **Chapter 3**

Conference Papers:

1. **<u>Ang-Chih Hsieh</u>** and TingTing Hwang, "Thermal-Aware Memory Mapping in 3D Designs," *Design, Automation & Test in Europe Conference & Exhibition (DATE)*, pp. 1361-1366, 20-24, April 2009.
   - **Chapter 3**

2. **<u>Ang-Chih Hsieh</u>**, TingTing Hwang, Ming-Tung Chang, Min-Hsiu Tsai, Chih-Mou Tseng and Li, Hung-Chun Li, "TSV Redundancy: Architecture and Design Issues in 3D IC," *Design, Automation & Test in Europe Conference & Exhibition (DATE)*, pp. 161-171, 8-12, March, 2010.
   - **Chapter 4**

3. **<u>Ang-Chih Hsieh</u>** and TingTing Hwang, "Run-time Reconfiguration of Expandable Cache for Embedded Systems," *International Symposium on VLSI Design Automation and Test (VLSI-DAT)*, pp. 207-210, 26-29, April 2010.
   - **Chapter 2**

4. **<u>Ang-Chih Hsieh</u>**, Chun-Cheng Liu and TingTing Hwang, "Enhanced Heterogeneous Code Cache Management Scheme for Dynamic Binary Translation," to appear in *Asia and South Pacific Design Automation Conference (ASP-DAC)*, 2011.

94 學年度入學 (博六)

Journal Publications

[1]. **C.-K. Chiang**, W.-H. Pan, C. Hwang, S. Zhuang and S.-H. Lai, "Fast H.264 Encoding Based On Statistical Learning," IEEE Transactions on Circuits and Systems for Video Technology (**TCSVT**), 2011 accepted, (SCI).
(Impact factor: 2.548)

[2]. **C.-K. Chiang**, S.-F. Wang, Y.-L. Chen and S.-H. Lai, "Fast JND-Based Video Carving with GPU Acceleration for Real-Time Video Retargeting," IEEE Transactions on Circuits and Systems for Video Technology (**TCSVT**), vol. 19, Issue 11, 2009, pp. 1588-1597. (SCI). **(Impact factor: 2.548, Acceptance rate 16.67% - Nominated for Best Paper Award**)

[3]. **C.-K. Chiang**, H.-C. Fei and S.-H. Lai, "Fast Multi-Reference Motion Estimation via Enhanced Downhill Simplex Search, "Journal of Signal Processing Systems", vol. 46, Issue 2-3, March 2007, pp. 75-86.
(Impact factor: 0.533)

Conference Publications

[4]. **C.-K. Chiang**, C.-S. Duan and S.-H. Lai, "Learning Component-Level Sparse Representation Using Histogram Information for Image Classification," The International Conference on Computer Vision (ICCV), 2011. (submitted)

[5]. **C.-K. Chiang** and S.-H. Lai, "Fast H.264 Encoding Based On Statistical Learning," The 2010 Pacific-Rim Conference on Multimedia (PCM), Shanghai, China, 2010. (**Oral paper**)

[6]. S.-F. Wang, Y.-L. Chen, **C.-K. Chiang** and S.-H. Lai, "Content-Aware Geometry Image Resizing," Proc. Computer Graphics International (CGI), short paper, Singapore, June 2010.

[7]. **C.-K. Chiang** and S.-H. Lai, "Fast Multi-Reference Motion Estimation Via Statistical Learning For H.264," IEEE International Conference on Multimedia & Expo (ICME), New York, 2009. (**Oral paper**)

[8]. S.-F. Wang, Y.-L. Chen, **C.-K. Chiang** and S.-H. Lai, "Surface Simplification by Image Retargeting," SIGGRAPH Asia 2009 Sketches.

[9]. W.-H. Pan, **C.-K. Chiang**, S.-H. Lai, "Fast Intermode Decision via Statistical Learning for H.264 Video Coding," International Multi-Media Modeling Conference (MMM), Tokyo, 2008.

[10].W.-H. Pan, **C.-K. Chiang**, S.-H. Lai, "Adaptive Multi-Reference Downhill

Simplex Search Based On Spatial-Temporal Motion Smoothness Criterion," IEEE International Conference on Acoustics, Speech, and Signal Processing (ICASSP), 2007.

[11]. **C.-K. Chiang**, H.-C. Fei and S.-H. Lai, "Fast Multi-Reference Frame Motion Estimation via Downhill Simplex Search," IEEE International Conference on Multimedia & Expo (ICME), Canada, 2006.

100 年 4 月 通過 學術審查

94 學年度入學 (博六)

Journal Papers:

1. **Zhi-Sheng Chen**, and Jyh-Shing Roger Jang, "On the Use of Anti-word Models for Audio Music Annotation and Retrieval," *IEEE Trans. on Audio, Speech, and Language Processing*, volume 17, issue 8, pp. 1547-1556, 2009.
   - ➢ Impact factor: 1.782
   - ➢ Google Scholar Citation: 4
   - ➢ Chapter: 2, 3
2. **Zhi-Sheng Chen,** Jyh-Shing Roger Jang, and Chin-Hui Lee, "A Kernel Framework for Content-Based Artist Recommendation System in Music," submitted to *IEEE Trans. on Multimedia*.
   - ➢ Impact factor: 2.288
   - ➢ Chapter: 4, 5

Conference Papers:

1. **Zhi-Sheng Chen**, Jia-Min Ren, and Jyh-Shing Roger Jang, "Music Annotation and Retrieval System Using Anti-Models," *Proceedings of 125th American Engineering Society Convention (AES),* San Francisco, USA, Oct. 2008.
2. **Zhi-Sheng Chen**, Jia-Min Zen, and Jyh-Shing Roger Jang, "Music Annotation System for Special TAGATUNE Submission," extended abstract in *International Society for Music Information Retrieval (ISMIR)*, Kobe, Japan, 2009.
3. Jia-Min Ren, **Zhi-Sheng Chen,** and Jyh-Shing Roger Jang, "Using Sequential Patterns as Short-Term and Long-Term Features for Audio Genre Classification," extended abstract in *International Society for Music Information Retrieval (ISMIR)*, Kobe, Japan, 2009.
4. Jia-Min Ren, **Zhi-Sheng Chen,** and Jyh-Shing Roger Jang, "On the Use of Sequential Patterns Mining as Temporal Features for Music Genre Classification," *The 35th International Conference on Acoustics, Speech, and Signal Processing (ICASSP)*, Dallas, Texas, March 2010.

93 學年度入學 (博七)

Journal Paper

1. **Chia-Hui Wei**, Min-Shiang Hwang, Augustin Yeh-Hao Chin, "A Mutual Authentication Protocol For RFID," accepted and to appear in *IEEE IT Professional.*

2. **Chia-Hui Wei**, Min-Shiang Hwang, Augustin Yeh-Hao Chin, "A New Authentication Protocol for Serverless RFID," accepted and to appear in *International Journal of Innovative Computing, Information and Control.*
   (SCI, EI, 2009 Impact Factor: 2.932, Ranking: 14/103).

3. **Chia-Hui Wei**, Min-Shiang Hwang, Augustin Yeh-Hao Chin, "An Authentication Protocol for Low-Cost RFID Tags," accepted and to appear in *International Journal of Mobile Communications.*
   (SSCI, Ranking: 45/130).

4. **Chia-Hui Wei**, Augustin Yeh-Hao Chin, "An Adaptive Group Location in Location Management for Mobile Network," *Information Sciences* (2011 Under the review, after the revised)
   (SCI, EI, 2009 Impact factor: 3.291, Rank: 6/116)

5. Chun-Ta Li, **Chia-Hui Wei**, Augustin Yeh-Hao Chin, "A Secure Event Update Protocol for Peer-To-Peer Massively Multiplayer Online Games Against Masquerade Attacks," *International Journal of Innovative Computing, Information and Control*, Vol. 5, no. 12(A), pp. 4715-4723, 2009.
   (SCI, EI, 2009 Impact Factor: 2.932, Ranking: 14/103).

6. Chun-Ta Li, **Chia-Hui Wei**, Cheng-Chi Lee, Augustin Yeh-Hao Chin, Lian-Jun Wang, "A Secure and Undeniable Billing Protocol among Charged Parties for Grid Computing Environments," *International Journal of Innovative Computing, Information and Control*, Vol. 6, no. 11, pp. 5061-5076, 2010.
   (SCI, EI, 2009 Impact Factor: 2.932, Ranking: 14/103).

7. **Chia-Hui Wei**, Augustin Yeh-Hao Chin, Cryptanalysis of an Efficient and Secure Event Signature Protocol for Peer-to-Peer Massively Multiplayer Online Games, *International Journal of Security and Its Applications*, volume 3, number 3, July 2009, pp. 1 – 8. (EI)

8. Min-Shiang Hwang, **Chia-Hui Wei**, and Cheng-Yee Lee, Privacy and Security Requirements for RFID Applications, *Journal of Computers*, Vol.20 No.3 October 2009.

Conference Paper

1. **<u>Chia-Hui Wei</u>**, Augustin Yeh-Hao Chin, Chun-Ta Li, "A Secure Billing Protocol for Grid Computing," 6th International Conference on Information Technology: New Generations (ITNG 2009), pp. 320-325, April 27-29, 2009, Las Vegas, Nevada, USA. (EI)