

## 102 年 10 月 通過 學術審查

年 級：博\_7\_ (96 入學)

### 著作列表

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#### Journal Papers

1. **Kenn Slagter**, Ching-Hsien Hsu, and Yeh-Ching Chung, "An Adaptive Sampling Mechanism for Partitioning in MapReduce," To appear in *International Journal of Parallel Programming*.
2. **Kenn Slagter**, Ching-Hsien Hsu, and Yeh-Ching Chung, Daqiang Zhang, "An Improved Partitioning Mechanism for Optimizing Massive Data Analysis Using MapReduce," *The Journal of Supercomputing*, Vol. 66, No. 1, October 2013, pp. 539-555.
3. Ching-Hsien Hsu, **Kenn Slagter**, Shih-Chang Chen, Yeh-Ching Chung, "Optimizing Energy Consumption with Task Consolidation in Clouds," *Information Sciences*, online December 2012.

#### Conference Papers

1. **Kenn Slagter**, Ching-Hsien Hsu, and Yeh-Ching Chung, "Dynamic Data Partitioning and Virtual Machine Mapping: Efficient Data Intensive Computation with MapReduce," To appear in *Proceedings of IEEE International Conference on Cloud Computing Technology and Science (CloudCom)*, 2013; acceptance rate: 17.8%
2. **Kenn Slagter**, Ching-Hsien Hsu, Yeh-Ching Chung and Jong Hyuk Park, "Network-Aware Multiway Join for MapReduce", *Proceedings of the 8th International Grid and Pervasive Computing (GPC 2013)*, LNCS 7861, May 2013, pp. 73-80
3. Meng-Hsuan Cheng, **Kenn Slagter**, Tai-Wen Lung, and Yeh-Ching Chung, "A VLIW-Based Post Compilation Framework for Multimedia Embedded DSPs with Hardware Specific Optimizations", *Proceedings of Methods and Tools of Parallel Programming Multicomputers - Second Russia-Taiwan Symposium, MTPP 2010*, Vladivostok, Russia, May 16-19, 2010, LNCS 6083, pp. 51-58

## **In Progress of Submission**

1. **Kenn Slagter**, Ching-Hsien Hsu, and Yeh-Ching Chung, “Locality and Loading Aware Virtual Machine Mapping Techniques for Optimizing Communications” in *International Journal of Grid Computing and eScience : Future Generation Computer Systems (FGCS)*
2. **Kenn Slagter**, Ching-Hsien Hsu, and Yeh-Ching Chung, “SmartJoin: A Network-Aware Multiway Join for MapReduce” in *Journal of Cluster Computing*

## 102 年 10 月 通過 學術審查

年 級：博六 (95 碩入，97 上直升)

### 著作列表

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#### Journal Papers

1. Po-Ting Wei, Tai-Chi Wang, Shih Yu Chang and Yeh-Ching Chung, "Computing Traffic Information in the cloud", accepted by International Journal of Grid and High Performance Computing, 2013.
2. B. Y. Liu, Shih Yu Chang, and Tai-Chi Wang, "Joint improvements for capacity and power efficiency of mobile WiMAX by relaying schemes." Wireless Communications and Mobile Computing, 2011.
3. Tai Chi Wang, Shih Yu Chang and Hsiao-Chun Wu, "Optimal energy-efficient pair-wise cooperative transmission scheme for wimax mesh networks," IEEE Journal on Selected Areas in Communications, vol.27, no.2, pp.191-201, February 2009.

#### Conference Papers

1. Tai-Chi Wang and Shih Yu Chang, "Enhanced Prediction-based Routing Protocol with Game Theory Adoption", accepted by International Computer Science and Engineering Conference (ICSEC), 2013.
2. Tai-Chi Wang and Shih Yu Chang, "An Analytical Model for Interactive VANET Services," Services Computing Conference (APSCC), 2011 IEEE Asia-Pacific , vol., no., pp.368-372, 12-15 Dec. 2011.
3. Vin Sen Feng, Tai Chi Wang, Shih Yu Chang and Hsi-Pin Ma, "Location estimation in indoor wireless networks by hierarchical support vector machines with fast learning algorithm," System Science and Engineering (ICSSE), 2010 International Conference on, vol., no., pp.321-326, 1-3 July 2010.
4. Tai-Chi Wang, Shih Yu Chang and Hsiao-Chun Wu, "Novel Cooperative Transmission Using Matching Algorithm for WiMax Mesh Networks," International Wireless Communications and Mobile Computing Conference, 2008. IWCMC '08. , vol., no., pp.655-660, 6-8 Aug. 2008.

## 102年10月通過學術審查

年 級：博六（97博入）

### 著作列表

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#### Journal Papers

1. **Fu-Wei Chen**, Shih-Liang Chen, Yung-Sheng Lin and TingTing Hwang, “A Physical-Location Aware X-bit Redistribution for Maximum IR-Drop Reduction,” IEEE Transactions on VLSI Systems 20(12): 2255-2264, 2012. (SCI, impact factor =1.218)
2. **Fu-Wei Chen** and TingTing Hwang, “Clock Tree Synthesis with Methodology of Re-Use in 3-D IC”, to be published in ACM Journal on Emerging Technologies in Computing Systems (accepted) (SCI, impact factor=0.758)

#### Conference Papers

1. **Fu-Wei Chen**, Hui-Ling Ting and TingTing Hwang, “Fault-tolerant TSV by Using Scan-chain Test TSV” to be accepted in Proceedings of ACM/IEEE Asia and South Pacific Design Automation Conference (ASP-DAC), 2014(EI) (108/343 ~ 31%)
2. **Fu-Wei Chen** and TingTing Hwang, “Clock Tree Synthesis with Methodology of Re-Use in 3-D IC,” to appear in Proceeding of ACM/IEEE Design Automation Conference (DAC), 2012 (EI) (164/741 ~ 22%)
3. **Fu-Wei Chen**, Shih-Liang Chen, Yung-Sheng Lin and TingTing Hwang, “A physical-location-aware fault redistribution for maximum IR-drop reduction,” in Proceedings of ACM/IEEE Asia and South Pacific Design Automation Conference (ASP-DAC), pp. 701-706, 2011. (EI) (104/300 ~ 35%)

#### In progress of submission

1. **Fu-Wei Chen**, Yi-Yu Liu, and TingTing Hwang, “Time-Multiplexed Dual Role TSV for Fault Tolerance at Module Level,” to be submitted in in Proceedings of ACM/IEEE Design, Automation and Test in Europe (DATE), 2014