

105 年 6 月 通過 學術審查

年 級：博九

著作列表

Journal Papers:

1. **S. T. Chen**, H. T. Chiao, S. Y. Chang, H. M. Sun, “Fast and Low-Complexity Encoding of Raptor Codes Based on Operation Lists for Known Source Block Lengths,” in *Multimedia Tools and Applications*, accepted on 2016/04/11.
(SCI, Impact Factor :**1.346**)
2. H. M. Sun, **S. T. Chen**, J. H. Yeh and C. Y. Cheng, “A Shoulder Surfing Resistant Graphical Authentication System,” in *IEEE Transactions on Dependable and Secure Computing*, accepted on 2016/02/15.
(SCI, Impact Factor :**1.351**)

In progress of submission:

1. **S. T. Chen**, H. T. Chiao, S. Y. Chang, H. M. Sun, “Personal Video Delivery with Rapid Raptor Code Decoder,” in *Personal and Ubiquitous Computing*, revised on 2016/05/07.
(SCI, Impact Factor :**1.518**)

Conference Papers:

1. **S. T. Chen**, H. T. Chiao, S. Y. Chang, H. M. Sun, “Accelerating Raptor Code Decoding Using Group Parameters for IPTV Service Delivery,” In Proceedings of *The 5th International Symposium on Cloud and Service Computing*, 2015.
2. A. Rezapour, **S. T. Chen** and H. M. Sun, “Efficient Common Prefix Search on Encrypted Data as an Additional Service on the Top of the Storage Providers,” In Proceedings of *The 5th International Symposium on Cloud and Service Computing*, 2015
3. **S. T. Chen**, H. T. Chiao, S. Y. Chang, H. M. Sun and P. S. Zeng, “An HD Streaming System for WiFi Multicast Channels Based on Application-layer FEC,” *2013 IEEE International Symposium on Consumer Electronics (ISCE)*, Hsinchu, 2013, pp. 85-86.
4. **S. T. Chen**, H. T. Chiao, S. Y. Chang, H. M. Sun and C. H. Ho, “Efficient Encoding of Systematic Raptor Codes Based on Operation Lists for Known

Block Lengths,” *2013 IEEE Wireless Communications and Networking Conference (WCNC)*, Shanghai, 2013, pp. 4689-4694.

5. C. M. Chen, H. T. Chiao, **S. T. Chen** and H. M. Sun, “A new Business Model in Pay-TV Broadcasting Systems and its Conditional Access System,” *TENCON 2010 - 2010 IEEE Region 10 Conference*, Fukuoka, 2010, pp. 797-802.

Patent:

1. **S. T. Chen**, H. T. Chiao, H. M. Sun and C. H. Ho, “Apparatus and method for accelerating the encoding of raptor codes,” *US Patent*: 8,839,067.
2. **S. T. Chen**, H. T. Chiao, S. Y. Chang, H. M. Sun, “Communication system, method and receiver applicable thereto,” *US Patent*: 9,264,181.
3. H. M. Sun, **S. T. Chen**, M. S. Yeh, “Multi-touch methods and devices,” *US Patent*: 9,336,376.
4. **陳宣同**, 焦信達, 孫宏民, 何家興, “猛禽碼之編碼加速裝置與方法,” *TW Patent*: I485992.
5. **陳宣同**, 焦信達, 張仕穎, 孫宏民, “通訊系統、其方法與其接收端,” *TW Patent*: I519143.
6. 孫宏民, **陳宣同**, 葉明昇, “多點觸碰方法與裝置,” *TW Patent*: I485992.

年 級：博 9

著作列表

Journal Papers

1. Fang-Ping Pai, I-Ching Hsu, and Yeh-Ching Chung. "Semantic web technology for agent interoperability: a proposed infrastructure." *Applied Intelligence* 44.1 (2016): 1-16 , (SCI, EI, Impact Factor: SJR-2014 1.474, SNIP-2014 1.965)

Conference Papers

1. Shih-Hsiang Lo, Cheng-An Chiu, Ding-Yong Hong, Fang-Ping Pai and Yeh-Ching Chung, "MGRID: A Modifiable -Grid Region Matching Approach for DDM in the HLA RTI," *Proceedings of ACM High Performance Computing Symposium of 2009 Spring Simulation Multiconference, (SpringSim 2009)*, No. 114.
2. Shih-Hsiang Lo, Fang-Ping Pai and Yeh-Ching Chung, "Offloading Region Matching of Data Distribution Management with CUDA," *Proceedings of IEEE International Conference on Intelligent Systems, Modeling and Simulation(ISMS2010)*, pp. 306-311.
3. Ding-Yong Hong, Fang-Ping Pai, Shih-Hsiang Lo and Yeh-Ching Chung. "A Scalable HLA RTI System Based on Multiple -FedServ Architecture." In *International Conference on Computer Modelling and Simulation (UKSim)*, March 2010, pp. 527-532.

In progress of submission

1. Fang-Ping Pai and Yeh-Ching Chung, "Multi-layer ontology based information fusion for situation awareness," submitted to *Applied Intelligence*. (SCI, EI, Impact Factor: SJR-2014 1.474, SNIP-2014 1.965), under minor revision.

105 年 06 月 通過 學術審查

年 級：博六 (98 碩入，99 下直升)

著作列表

Journal Papers

1. **Chung-Han Chou**, Yen-Ting Lai, Yi-Chun Chang, Chih-Yu Wang, Liang-Chia Cheng, Shih-Hsu Huang, Shih-Chieh Chang, "Ping-Pong Mesh: A New Resonant Clock Design for Surge Current and Area Overhead Reduction," *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (IEEE TCAD)*, 2016.
2. **Chung-Han Chou**, Hua-Hsin Yeh, Shih-Hsu Huang, Yow-Tyng Nieh, Shih-Chieh Chang, and Yung-Tai Chang, "Skew Minimization with Low Power for Wide-Voltage-Range Multi-Power-Mode Designs," *IEEE Transactions on Very Large Scale Integration Systems (IEEE TVLSI)*, pp. 1189-1192, April, 2016.

Conference Papers

1. **Chung-Han Chou**, Zhi-Yang Wang, Tsui-Yun Chang, Shih-Hsu Huang, Shih-Chieh Chang, "2.5D System Synthesis Methodology under Performance, Power and Thermal Constraints," in *Int. Symposium on VLSI Design, Automation and Test (VLSI-DAT)*, 2016.
2. Wen-Pin Tu, **Chung-Han Chou**, Shih-Hsu Huang, Shih-Chieh Chang Yow-Tyng Nieh and Chien-Yung Chou, "Low-Power Timing Closure Methodology for Ultra-Low Voltage Designs," in *Proc. of International Conference on Computer Aided Design (ICCAD)*, pp. 697-704, Nov, 2013.
3. **Chung-Han Chou**, Nien-Yu Tsai, Hao Yu, Yiyu Shi, Jui-Hung Chien, and Shih-Chieh Chang, "On the futility of thermal through-silicon-vias", in *Int. Symposium on VLSI Design, Automation and Test (VLSI-DAT)*, pp. 1-6, Apr, 2013.
4. **Chung-Han Chou**, Nien-Yu Tsai, Hao Yu, Che-Rung Lee, Yiyu Shi and Shih-Chieh Chang "On the Preconditioner of Conjugate Gradient Method - A Power Grid Simulation Perspective," in *Proc. of International Conference on Computer Aided Design (ICCAD)*, pp. 494-497, Nov, 2011.
5. C.L. Lung, Z.Y. Zeng, **Chung-Han Chou**, and Shih-Chieh Chang "Clock Skew Optimization Considering Complicated Power Modes," in *Proc. of Design, Automation and Test in Europe (DATE)*, pp. 1474-1479, Mar, 2010.

2016 年 06 月 通過 學術審查

年 級：博六（98 碩入，99 下直升）

著作列表

Journal Papers

1. [Yu-Guang Chen](#), Hui Geng, Kuan-Yu Lai, Yiyu Shi, and Shih-Chieh Chang, "Multi-Bit Retention Registers for Power Gated Designs: Concept, Design and Deployment," in *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD)*, Volume:33, Issue: 4, pp. 507-518, April 2014.
2. [Yu-Guang Chen](#), Wan-Yu Wen, Yiyu Shi, Wing-Kai Hon, and Shih-Chieh Chang, "Novel Spare TSV Deployment for 3D ICs Considering Yield and Timing Constraints," in *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD)*, Volume:34, Issue: 4, pp.577-588, Dec. 2014.

Conference Papers

1. Ming-Chao Lee, [Yu-Guang Chen](#), Ding-Kei Huang, and Shih-Chieh Chang, "NBTI-Aware Power Gating Design", in *Proc. of Asia and South Pacific Design Automation Conference (ASP-DAC)*, pp.609-614, Yokohama, Japan, Jan. 2011.
2. Ming-Chao Lee, Yiyu Shi, [Yu-Guang Chen](#), Diana Marculescu, and Shih-Chieh Chang, "Efficient on-line module-level wake-up scheduling for high performance multi-module designs", in *Proc. of ACM international symposium on International Symposium on Physical Design (ISPD)*, pp.97-104, Napa Valley, Mar. 2012.
3. [Yu-Guang Chen](#), Yiyu Shi, Kuan-Yu Lai, Geng Hui, and Shih-Chieh Chang, "Efficient Multiple-Bit Retention Register Assignment for Power Gated Design: Concept and Algorithms", in *Proc. of IEEE/ACM International Conference on Computer-aided Design (ICCAD)*, pp.309-316, San Jose, Nov. 2012.

4. [Yu-Guang Chen](#), Kuan-Yu Lai, Ming-Chao Lee, Yiyu Shi, Wing-Kai Hon, and Shih-Chieh Chang, “**Yield and Timing Constrained Spare TSV Assignment for Three-Dimensional Integrated Circuits**,” in *Proc. of Design, Automation & Test in Europe (DATE)*, pp.1-4, Dresden, Germany, March 2014.
5. [Yu-Guang Chen](#), Tao Wang, Kuan-Yu Lai, Wen-Yu Wen, Yiyu Shi, and Shih-Chieh Chang, “**Critical Path Monitor Enabled Dynamic Voltage Scaling for Graceful Degradation in Sub-Threshold**,” in *Proc. of IEEE/ACM Design Automation Conference (DAC)*, pp.1-6, San Francisco, CA, USA, March 2014.
6. [Yu-Guang Chen](#), Wan-Yu Wen, Tao Wang, Yiyu Shi and Shih-Chieh Chang, “**Q-Learning Based Dynamic Voltage Scaling for Designs with Graceful Degradation**,” in *Proc. of International Symposium on Physical Design (ISPD)*, pp.41-48, Monterey, CA, 2015.
7. [Yu-Guang Chen](#), Wan-Yu Wen, Yun-Ting Wang, You-Luen Lee, and Shih-Chieh Chang, “**A Novel Low-Cost Dynamic Logic Reconfigurable Structure Strategy for Low Power Optimization**,” accepted by *Asia and South Pacific Design Automation Conference (ASP-DAC)*, Macau, China, 2016.

Book chapter

1. [Yu-Guang Chen](#), Shi-Chieh Chang, and Yiyu Shi, “**Live Free or Die Hard: Design for Reliability in Three-Dimensional Integrated Circuits**,” a chapter of the book “*Physical Design for 3D Integrated Circuits*”, edited by Aida Todri-Sanial and Chuan Seng Tan, pp.193-227, CRC Publishing, 2015 (ISBN: 978-1498710367).

In progress of submission

1. [Yu-Guang Chen](#), Yun-Ting Wang, Jinglan Liu, Yiyu Shi, and Shi-Chieh Chang, “**Recycling Integrated Circuits for Reuse: The Concept and Algorithms Toward Green Semiconductor Industry**,” *IEEE/ACM International Conference on Computer-aided Design (ICCAD)* 2016. (submitted)
2. [Yu-Guang Chen](#), Shi-Chieh Chang, and Yiyu Shi. “**A Novel On-line Module-Level Wake-up Scheduling Algorithm for Designs with Multi-Modules**,” submitted to *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD)*. (submitted)

2016 年 06 月 不通過 學術審查

年 級：博七 (93 博入)

著作列表

Journal Papers

(J1) 共同作者, 共同作者, 共同作者, and A君. "Mobile Anchor-assisted Localization over Android". To appear in *Wireless Networks*. [SCI, EI, Impact Factor: 1.006.]

(J2) 共同作者, 共同作者, A君, and 共同作者. "Performance Evaluation of Classifier Ensembles in Terms of Diversity and Performance of Individual Systems". *International Journal of Pervasive Computing and communications*

(J3) A君, 共同作者, and 共同作者. "Comparing System Selection Methods for the Combinatorial Fusion of Multiple Retrieval Systems". *Journal of Interconnection Networks* (EI)

Conference Papers

(C1) A君, 共同作者, and 共同作者. "A Comparative Study on the Combination of Multiple Retrieval Systems." In *Proceedings of 2012 International Symposium on Pervasive Systems, Algorithms, and Networks*, IEEE Computer Society Press, San Marcos, Texas, USA. (EI)

In progress of submission

(S1) A君, 共同作者, 共同作者, and 共同作者. "Load Sharing for Inhomogeneous Radio Networks: A Combinatorial Fusion Approach". Under Revision.

(S2) 共同作者, A君, and 共同作者. "Handover-Supporting Streamlined Networking". Under Revision.