

## 99年2月通過學術審查

92 學年度碩士入學，93 學年度直升博士(博六)

### Journals

1. Enhancing Microkernel Performance on VLIW DSP Processors via Multiset Context Switch, **Kun-Yuan Hsieh**, Yung-Chia Lin, Chien-Ching Huang, and Jenq Kuen Lee, *Journal of Signal Processing Systems*, Vol. 51, No. 3, pp. 257-268, Springer, June, 2008.
  - JCR impact factor 2008: 0.779
  - Academic index: SCI , EI
  - Cited by 6:
2. Parallelization of Belief Propagation on Cell Processors for Stereo Vision, Kun-Yuan Hsieh, Chi-Hua Lai, Shang-Hong Lai, Jenq Kuen Lee, Accepted, ACM Transaction in Embedded Computing Systems.
  - Academic index: EI

### Conferences

1. pTest: An Adaptive Testing Tool for Concurrent Software on Embedded Multicore Processors, Shou-Wei Chang, **Kun-Yuan Hsieh**, Jenq Kuen Lee, *Proceedings of Design, Automation, and Test in Europe (DATE '09)*, pp. 1012-1017, Nice, France, 20-24, Apr. 2009. (Acceptance rate: 27%)
  - Academic index: EI
2. Support of Paged Register File for Improving Context Switching on Embedded Processors, Chung-Wen Huang, **Kun-Yuan Hsieh**, Jia-Jhe Li and Jenq Kuen Lee, *Proceedings of 7<sup>th</sup> IEEE/IFIP International Conference on Embedded and Ubiquitous Computing (EUC'09)*, Organized Under International Conference on Computational Science and Engineering (CSE '09), Vancouver, Canada, August 29-31, 2009
  - Academic index: EI
3. Software Cache Support and API Design for Embedded DSP Processors, Cheng-Yen Lin, Shao-Chung Wang, Ming-Yu Hung, **Kun-Yuan Hsieh**, Jenq Kuen Lee, *Proceedings of International SoC Design Conference 2009 (ISOCC 2009)*, 22-24, Nov, 2009, Busan, Korea.
  - Academic index: EI

4. Enabling Streaming Remoting on Embedded Dual-Core Processors, **Kun-Yuan Hsieh**, Yen-Chih Liu, Po-Wen Wu, Shou-Wei Chang, and Jenq Kuen Lee, *Proceedings of the 37th International Conference on Parallel Processing (ICPP '08)*, pp. 35-42, Portland, Oregon, 9-12 Sept. 2008. (Acceptance rate: 30%, 81 out of 263)
  - Academic index: EI
5. The Support of Software Design Patterns for Streaming RPC on Embedded Multicore Processors, **Kun-Yuan Hsieh**, Yen-Chih Liu, Chi-Hua Lai, Jenq Kuen Lee, *Proceedings of the 2008 IEEE Workshop on Signal Processing Systems (SiPS '08)*, pp. 263-268, Washington, D.C. , Metro Area, U.S.A., 8-10 Oct. 2008.
  - Academic index: EI
6. Integrating Compiler and System Toolkit Flow for Embedded VLIW DSP Processors, Chi Wu, **Kun-Yuan Hsieh**, Yung-Chia Lin, Chung-Ju Wu, Wen-Li Shih, S. C. Chen, Chung-Kai Chen, Chien-Ching Huang, Yi-Ping You, and Jenq Kuen Lee, *Proceedings of the 12th IEEE International Conference on Embedded and Real-Time Computing Systems and Applications (RTCSA '06)*, pp. 215-222, Sydney, August 16-18, 2006.
  - Academic index: EI

## Patents

1. Jenq-Kuen Lee, Shou-Wei Chang, **Kun-Yuan Hsieh**, Test Method and Tool for Master-Slave Concurrent System on Embedded Processors, US pending patent, Application No.:12/576,678.
2. 李政崑, 張守為, 謝坤員, 多核心處理器主從系統之測試方法與工具, Taiwan pending patent, Application No.: 098135219.
3. Jenq-Kuen Lee, **Kun-Yuan Hsieh**, Method of Streaming Method Invocation For Multicore Systems, US pending patent, Application No.: 12/571,986.
4. 李政崑, 謝坤員, 具資料串流功能之多核心遠端程序呼叫方法, US pending patent, Application No.: 097119465.
5. Jenq-Kuen Lee, **Kun-Yuan Hsieh**, Yung-Chia Lin, Method of Optimizing Multi-set Context Switch For Embedded Processors, US pending patent, Application No.: 11/742,322.
6. 李政崑, 謝坤員, 林永嘉, 最佳化嵌入式處理器之多集合內容切換之方法 Taiwan pending patent, Application No.: 096131014.

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93 學年入學 (博六)

### Journal Papers

1. Che-Lun Hung, Chihan Lee, Chun-Yuan Lin, Chih-Hung Chang, Yeh-Ching Chung, Chuan Yi Tang, "Feature Amplified Voting Algorithm for Functional Analysis of Protein Superfamily," accepted by *BMC Genomics*, 2010.(SCI) (2008 impact factor: 3.926, Rank: 24/144, Subject categories: BIOTECHNOLOGY & APPLIED MICROBIOLOGY)
2. Che-Lun Hung, Chun-Yuan Lin, Yeh-Ching Chung, Shu Ju Hsieh and Chuan Yi Tang, "CORAL-T: Heuristic CODing Region ALignment Method for Three Genome Sequences," *Communications of SIWN*, vol. 6, pp. 99-105, 2009.

### Conference Papers

1. Che-Lun Hung, Chun-Yuan Lin, Yeh-Ching Chung and Chuan Yi Tang, "A Parallel Algorithm for Three-Profile Alignment Method," *Workshop on Bioinformatics Algorithms, (in conjunction with The International Joint Conference on Bioinformatics, Systems Biology and Intelligent Computing)*, pp. 153-159, 2009. (EI, acceptance rate 22%)
2. Che-Lun Hung, Chun-Yuan Lin, Yeh-Ching Chung, Shu Ju Hsieh and Chuan Yi Tang, "CORAL-T: Heuristic CODing Region ALignment Method for Three Genome Sequences," *Proceedings of 2nd International Conference on Bioinformatics and Systems Biology* (CD only), 2009.
3. Che-Lun Hung, Chun-Yuan Lin, Yeh-Ching Chung and Chuan Yi Tang, "Introducing variable gap penalties into three-sequence alignment for protein sequences," *Proceedings of 22nd International Conference on Advanced Information Networking and Applications (AINA)*, pp. 726-731, 2008.
4. Che-Lun Hung, Chun-Yuan Lin, Yeh-Ching Chung and Chuan Yi Tang, "CrossWA: A new approach of combining pairwise and three-sequence alignments to improve the accuracy for highly divergent sequences alignment," *Proceedings of Sixth International Conference on Bioinformatics* (oral presentation) (*InCoB*), paper no. 5, pp. 30, 2007
5. Che-Lun Hung, Don-Lin Yang, Yeh-Ching Chung, and Ming-Chuan Hung, "A Novel Mining Algorithm for Periodic Clustering Sequential Patterns," *Lecture Notes in Computer Science, Advances in Applied Artificial Intelligence, IEA/AIE 2006*, Vol.4031, pp.1299-1308, 2006.